

FIG-1A
PRIOR ART

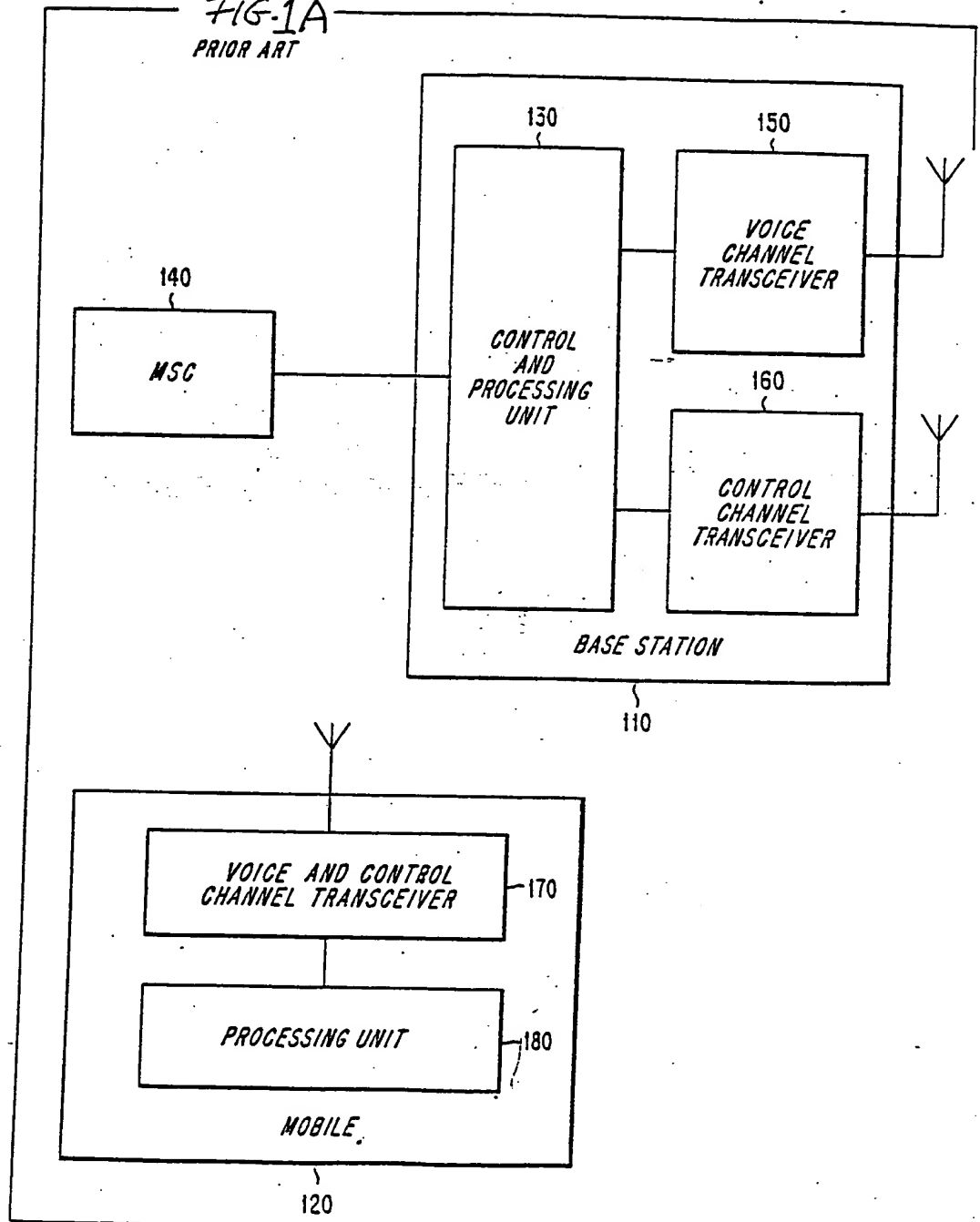


FIG. 1B

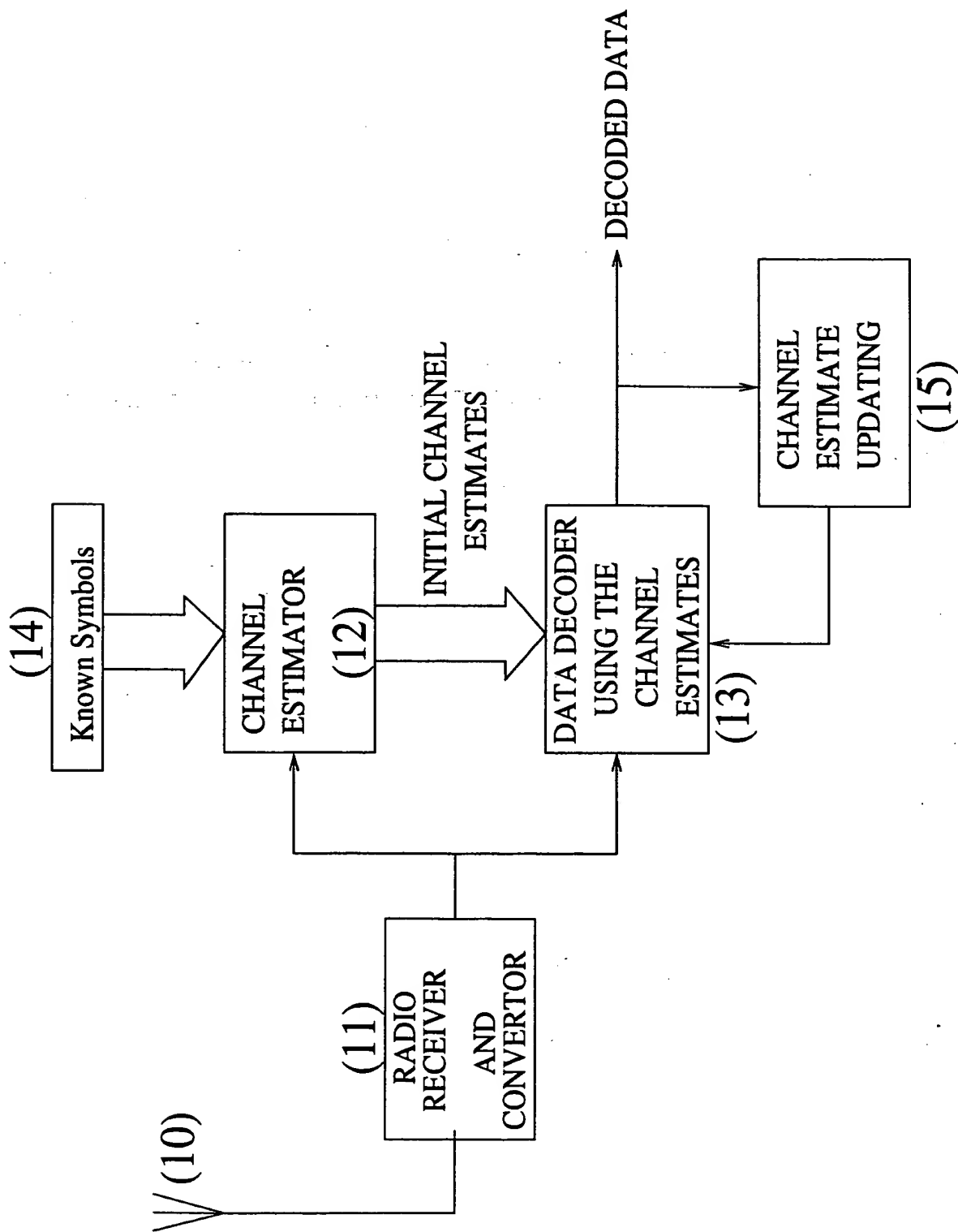
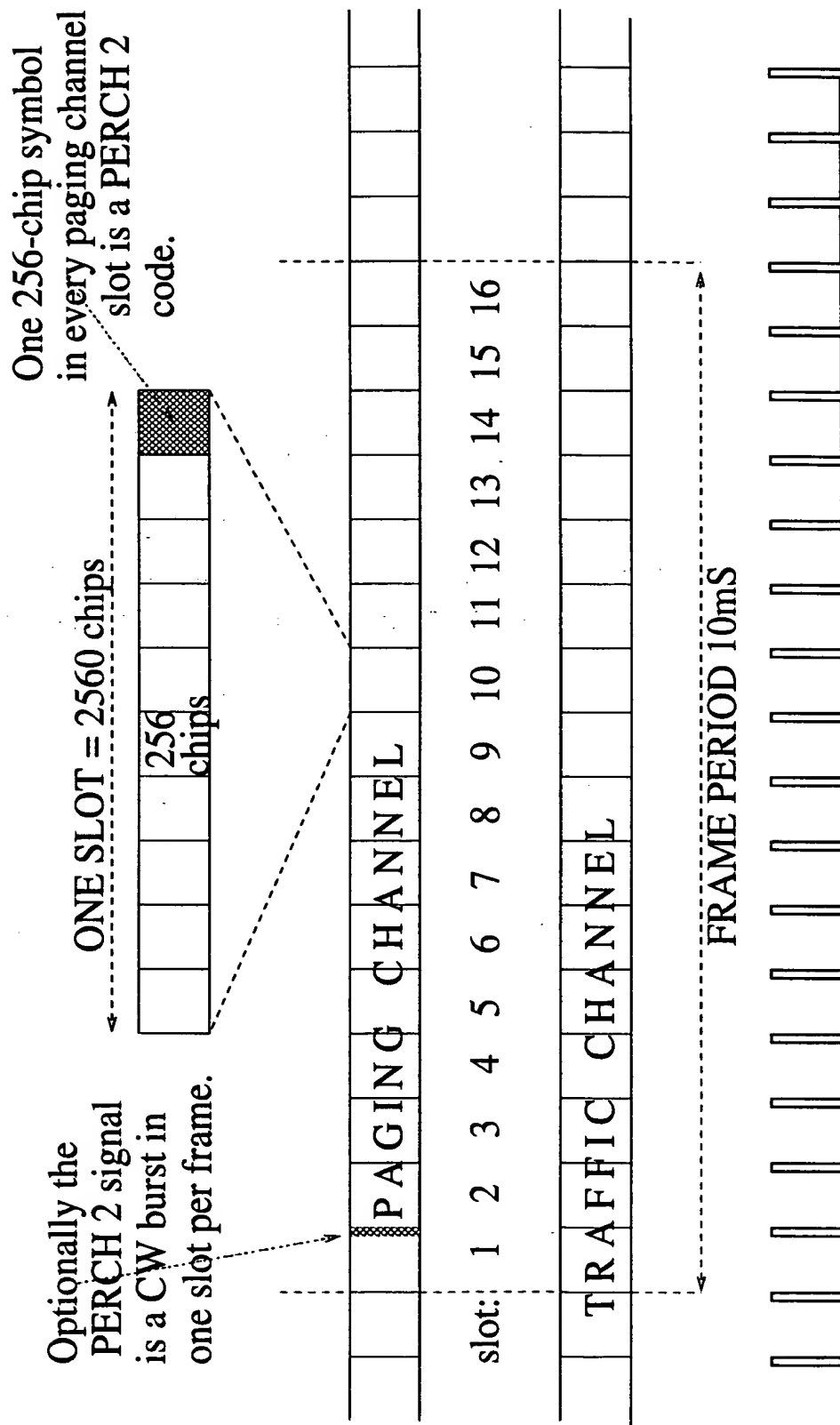


FIG. 2



PERiodic CHannel 1 (PERCH 1) = burst of a 256-chip known code in every 0.625mS slot

FIG. 3

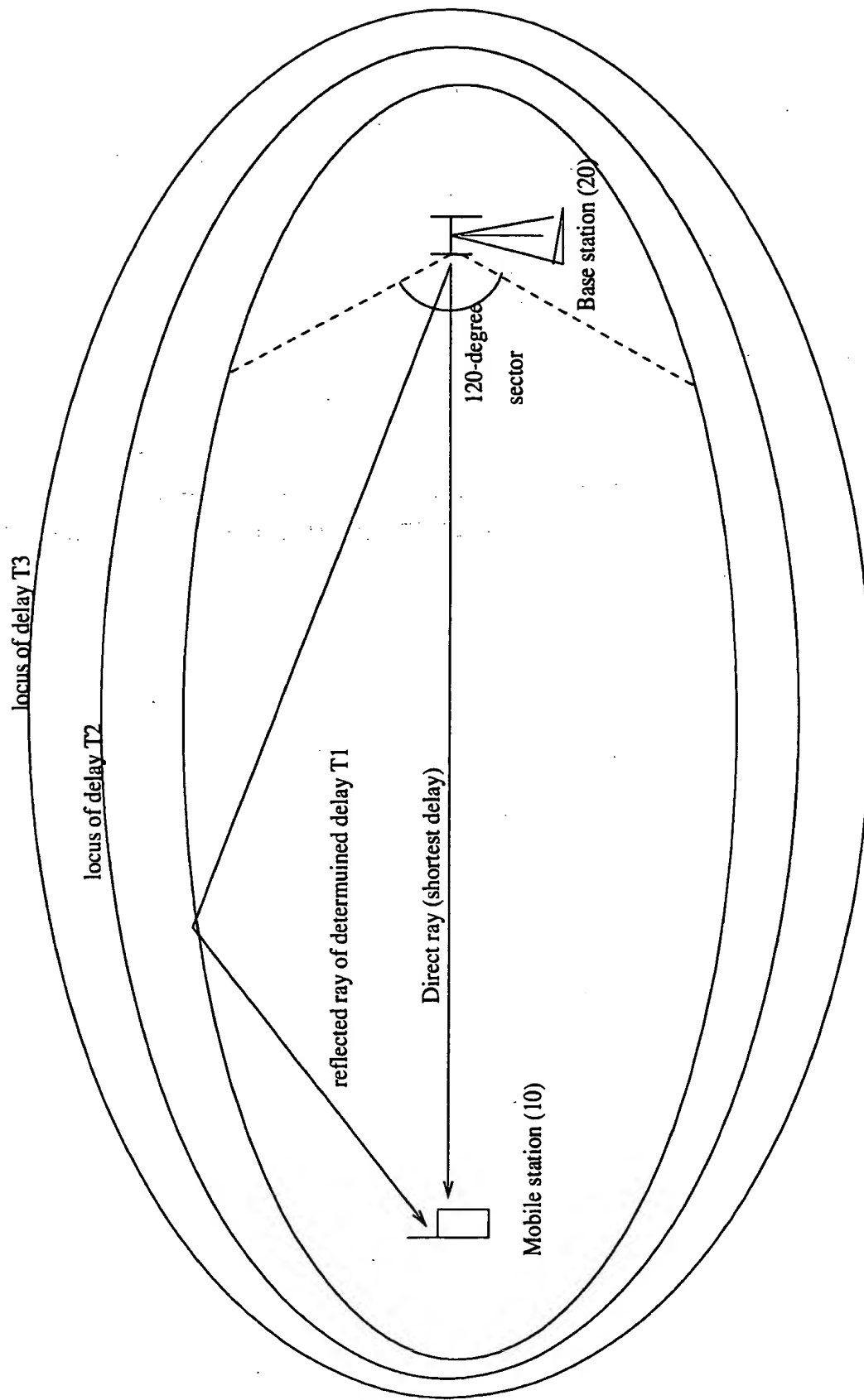


FIG. 4

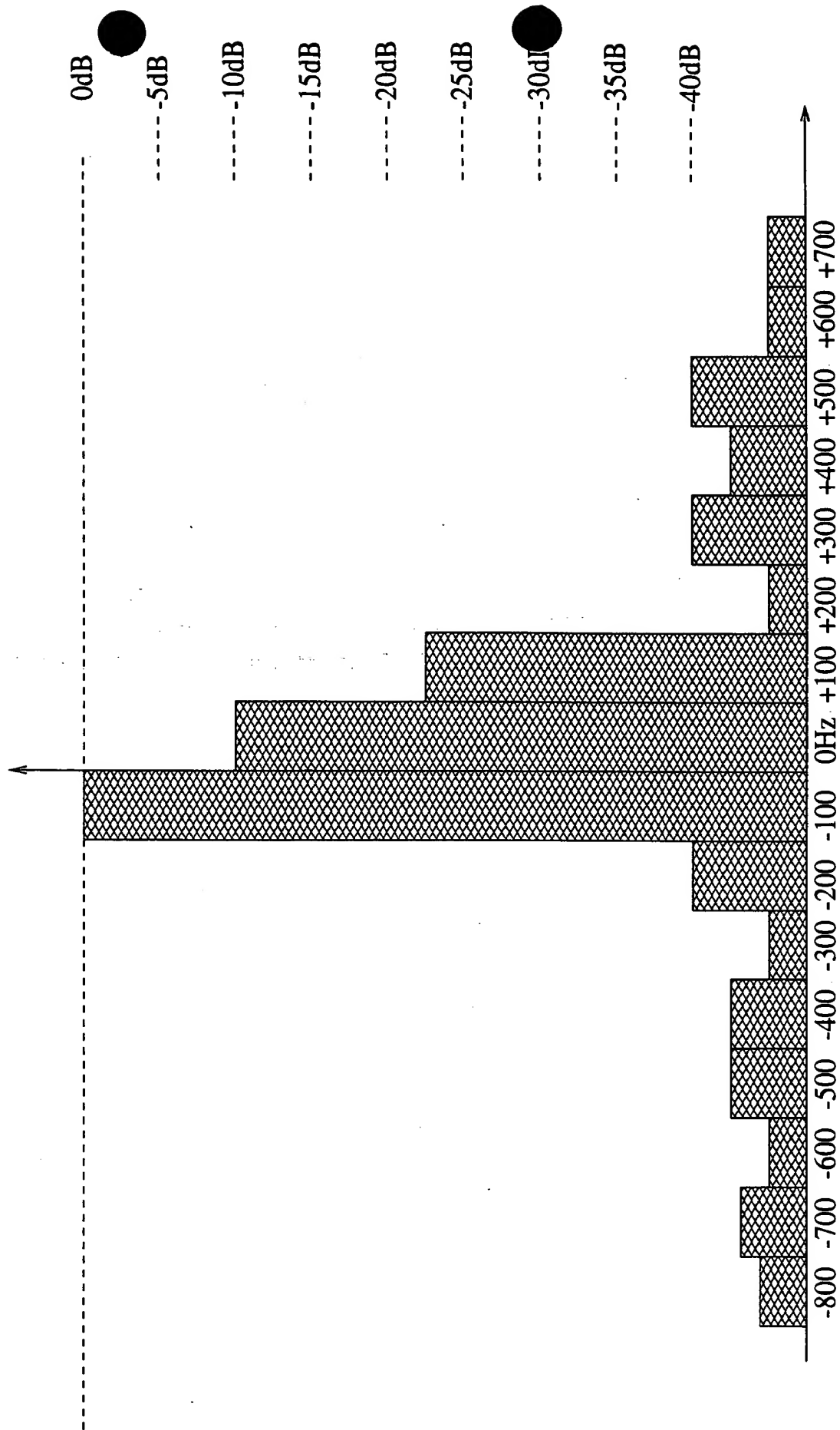


FIG. 5

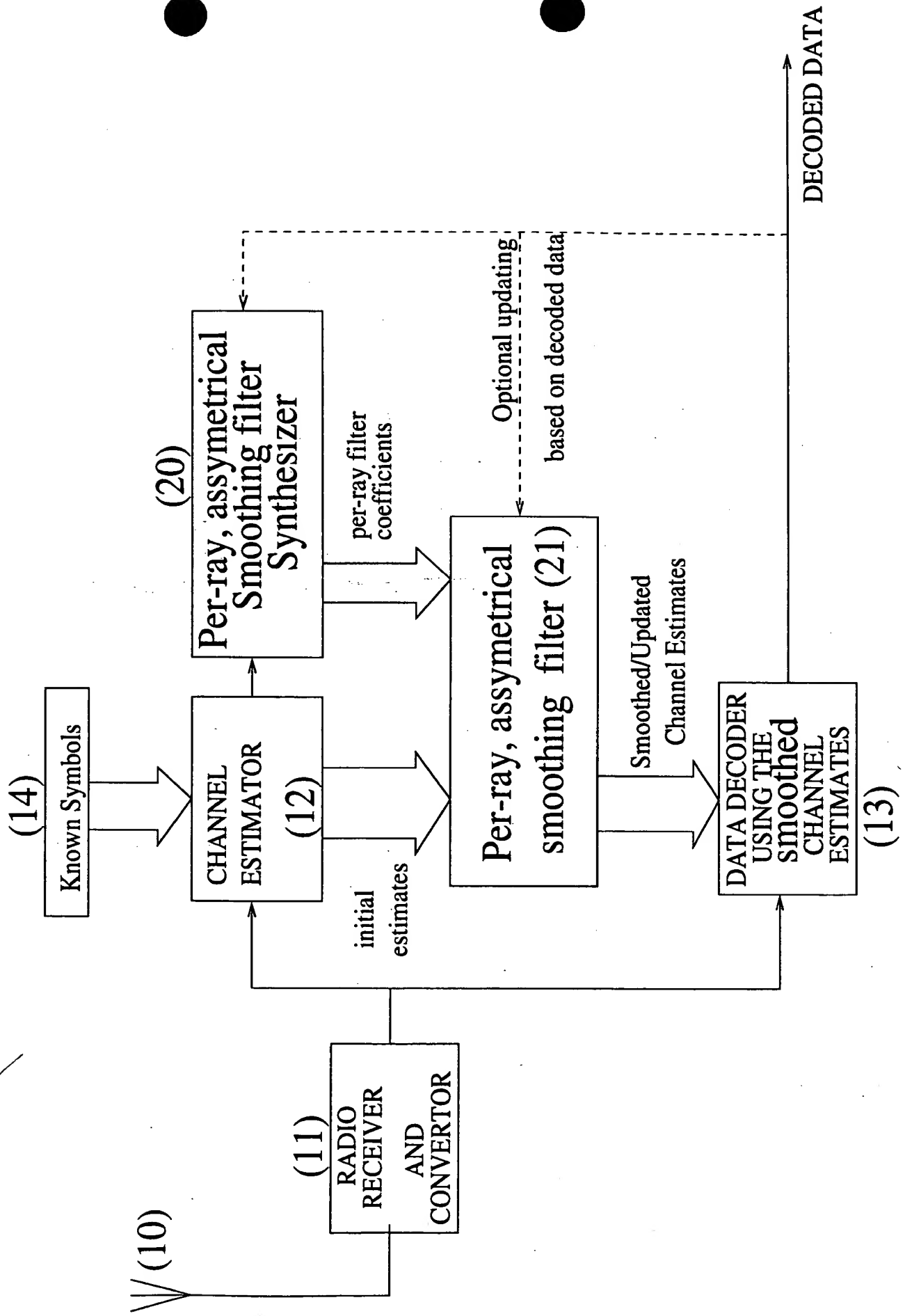


FIG. 6

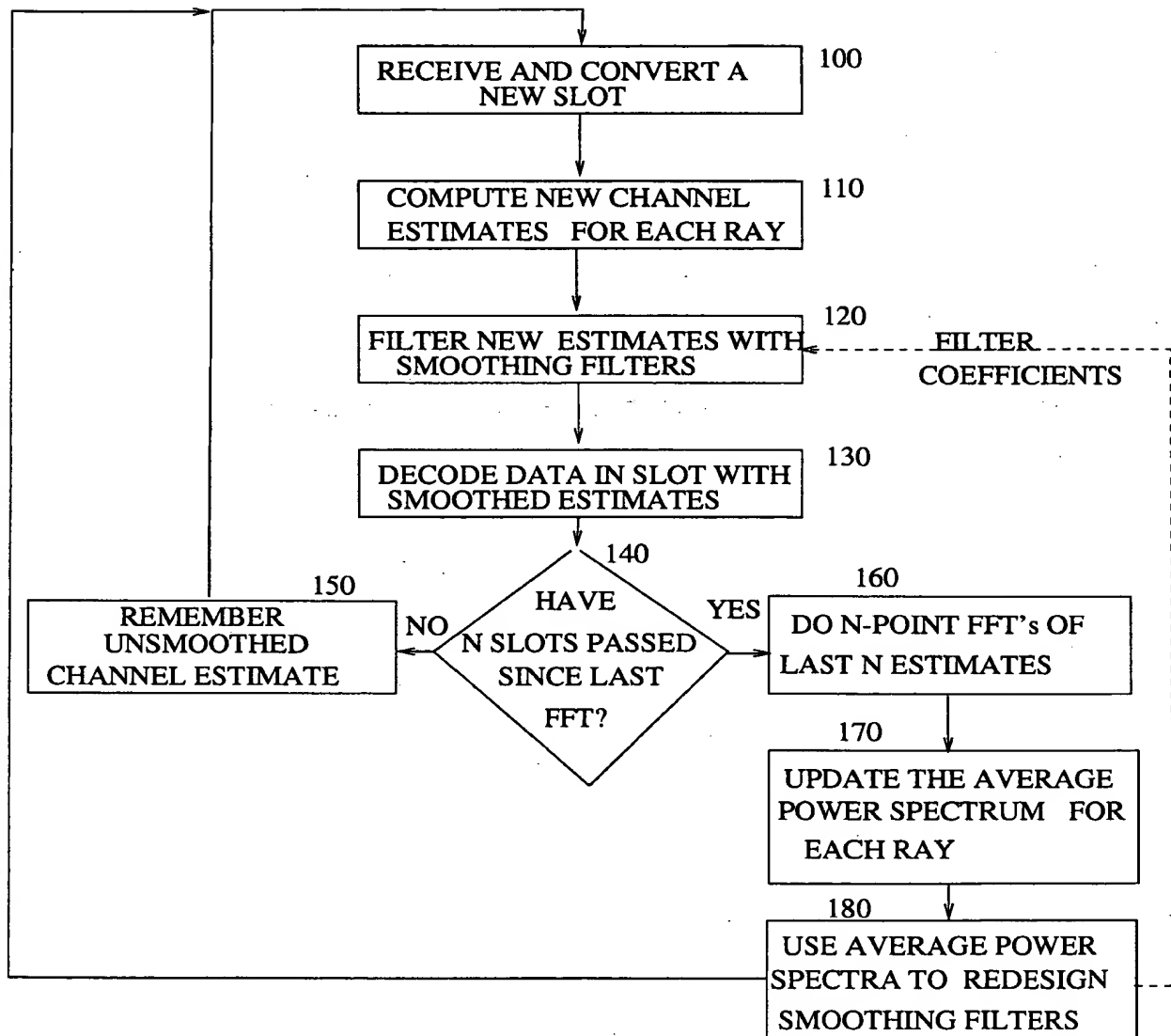
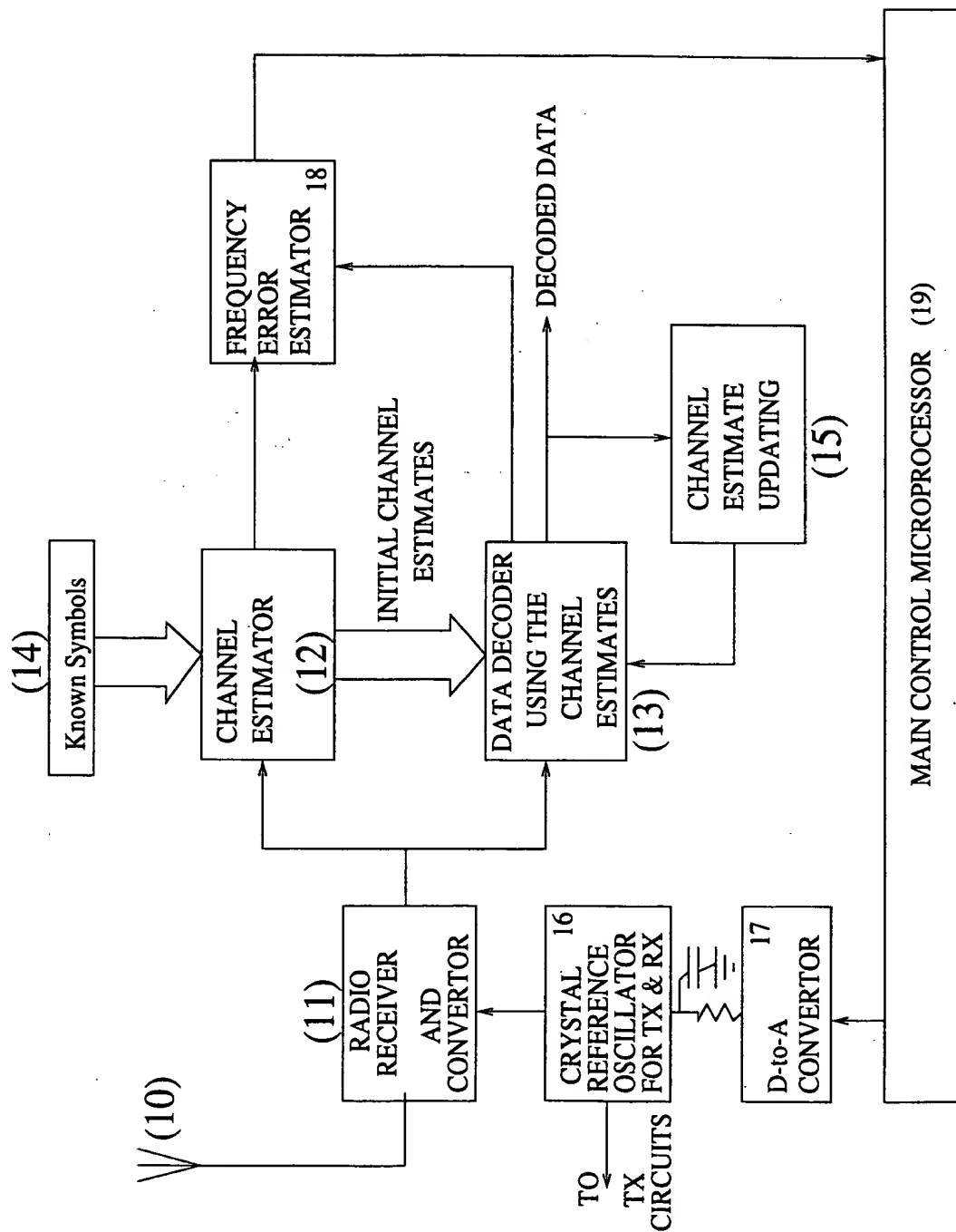


FIG. 7



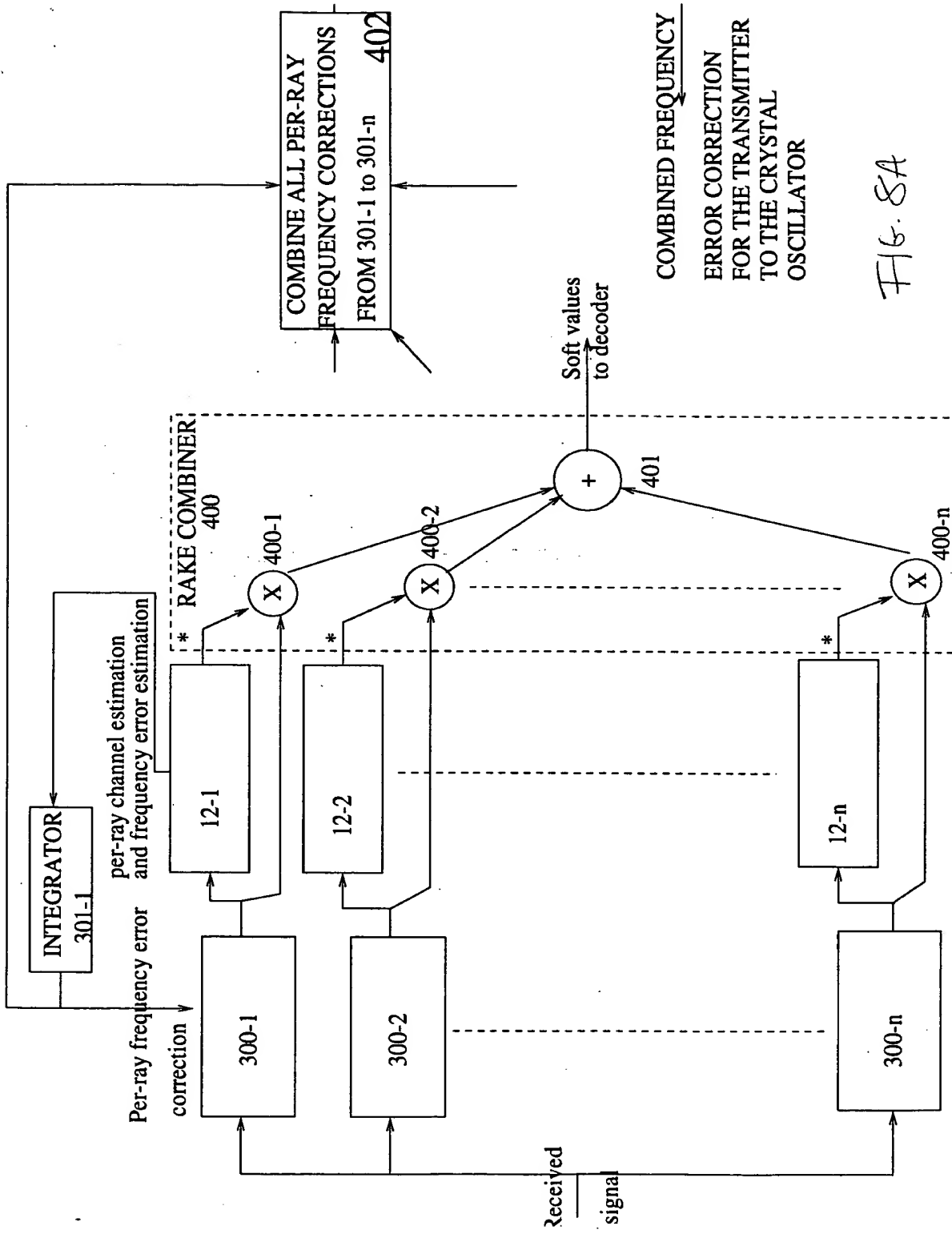


FIG. 8A

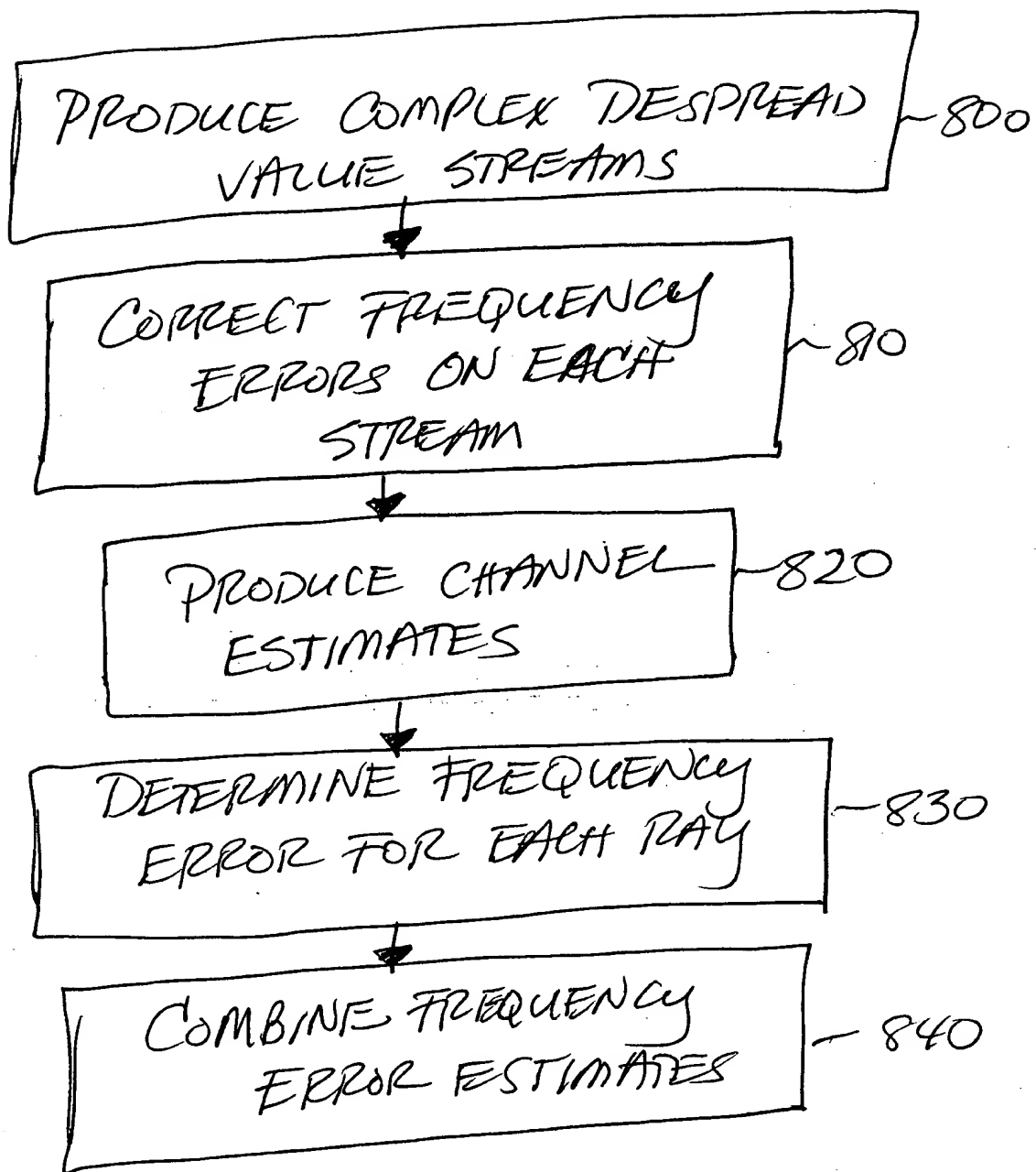
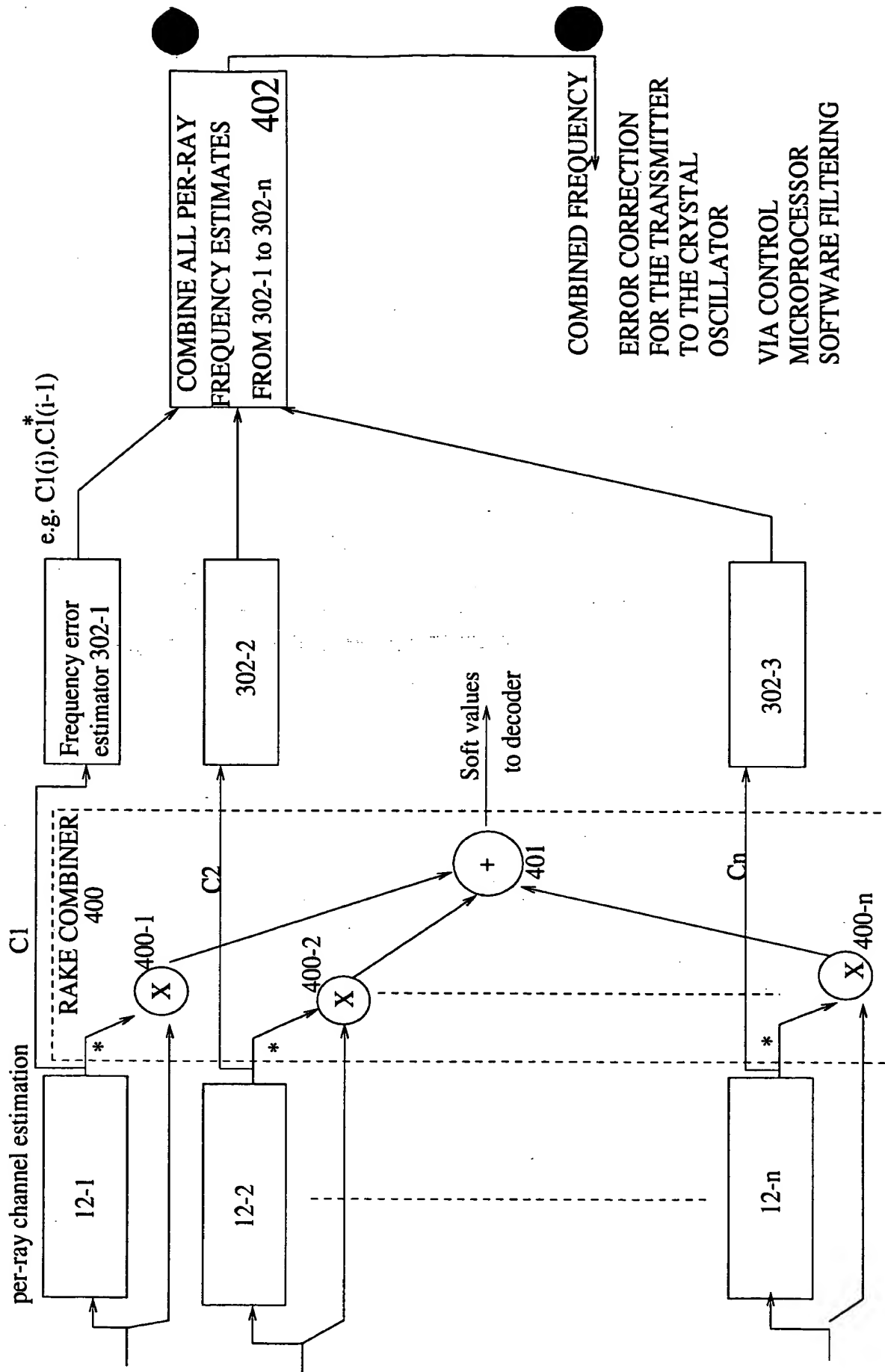


FIG. 8B

FIG. 9A



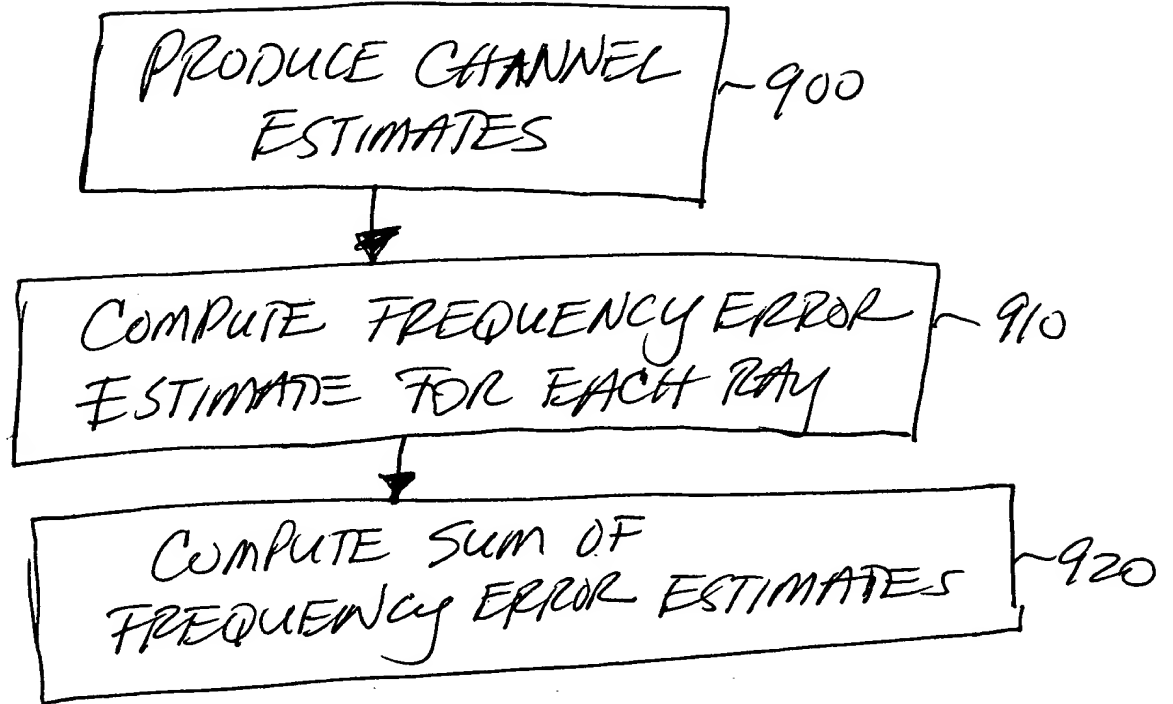


FIG. 9B

FIG. 10

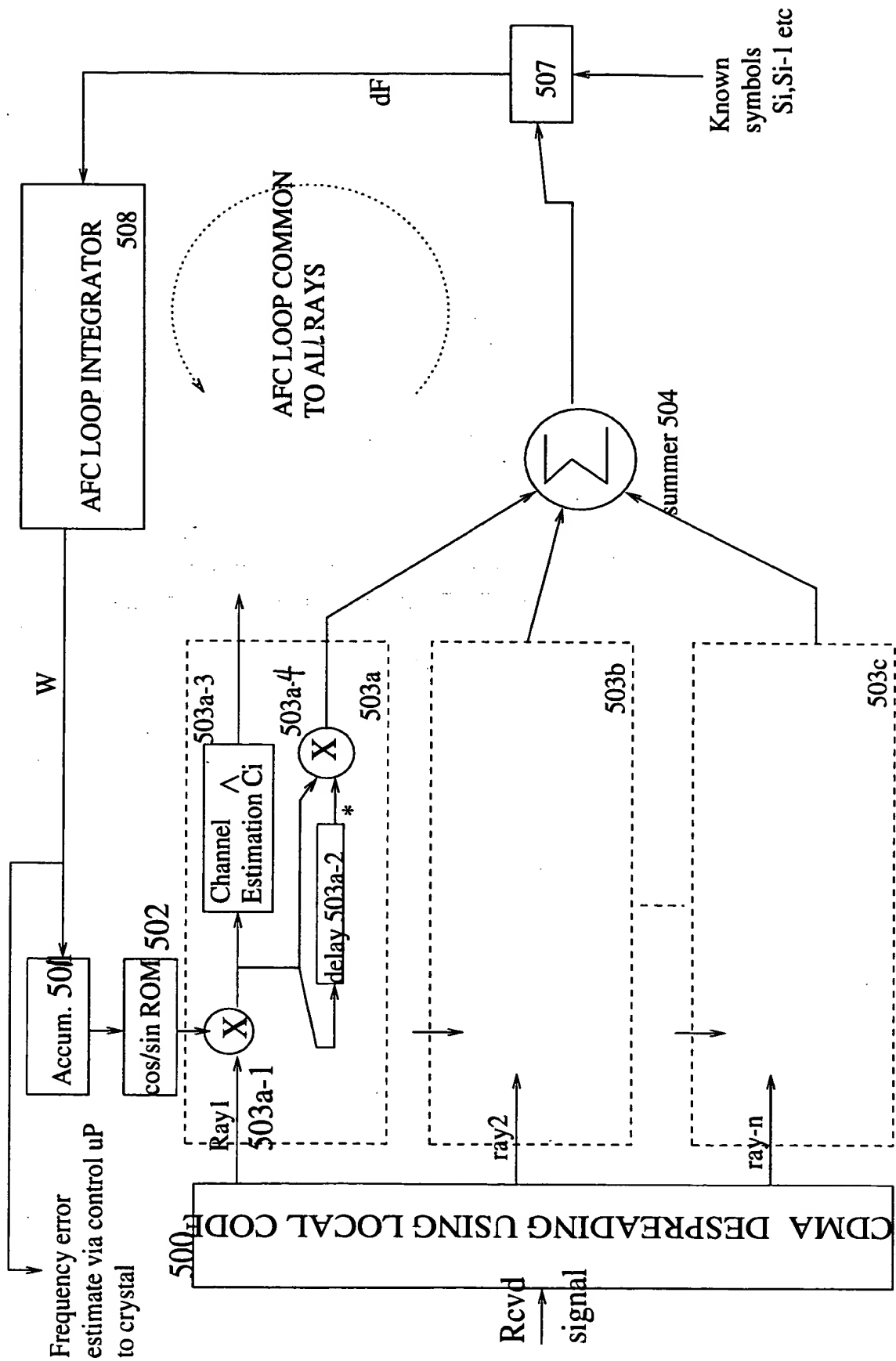


FIG. 11

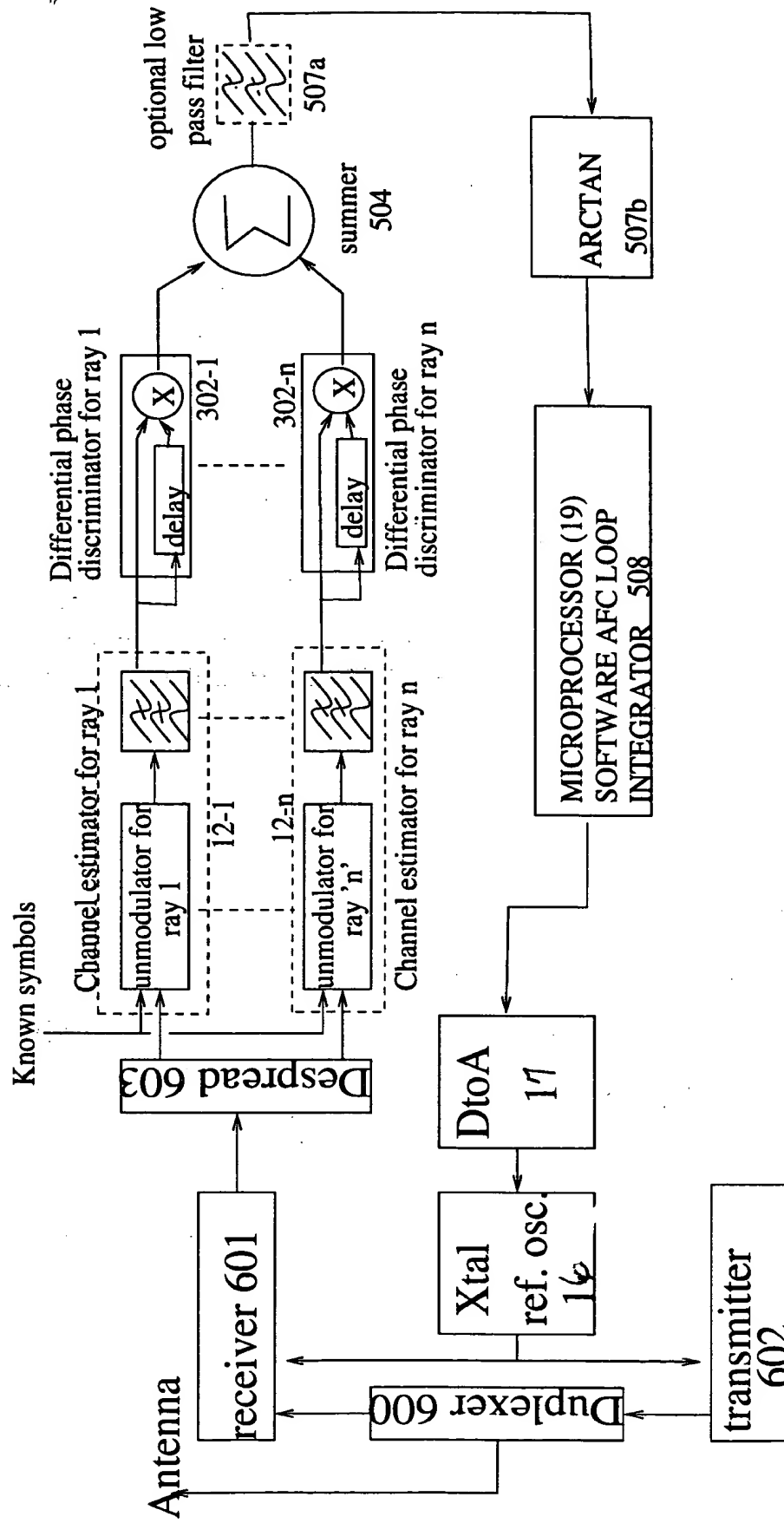


FIG. 12

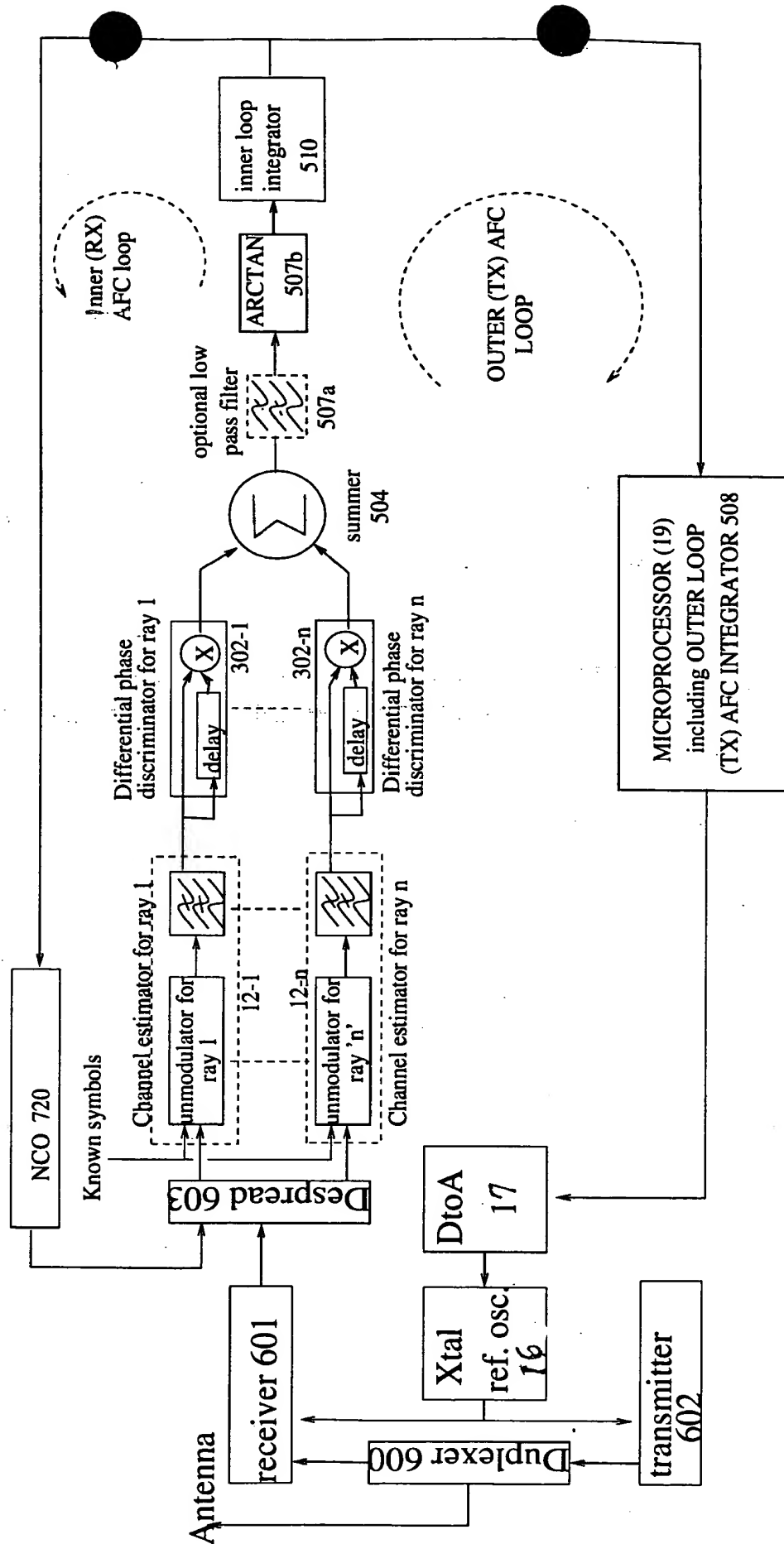


FIG. 13

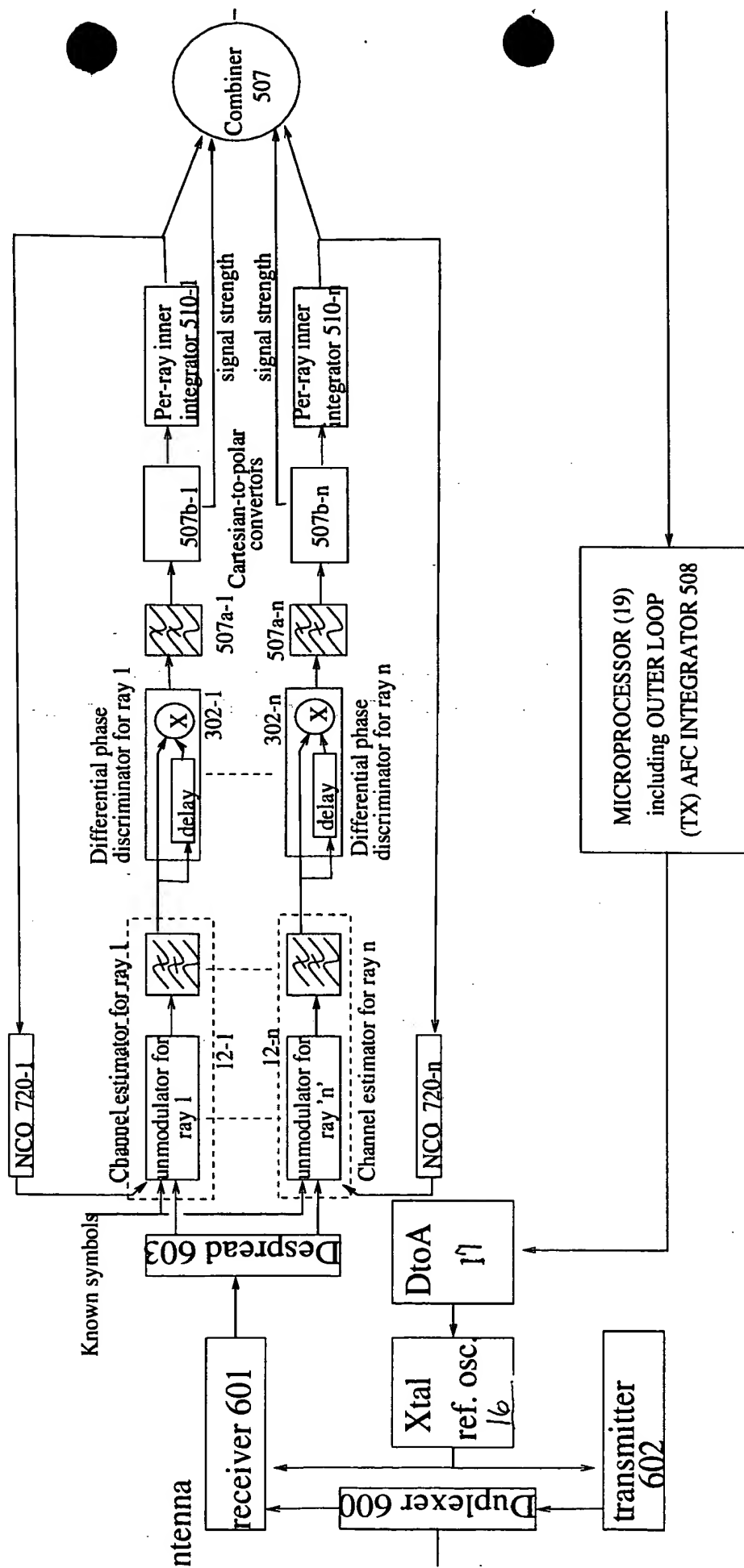


FIG. 14A

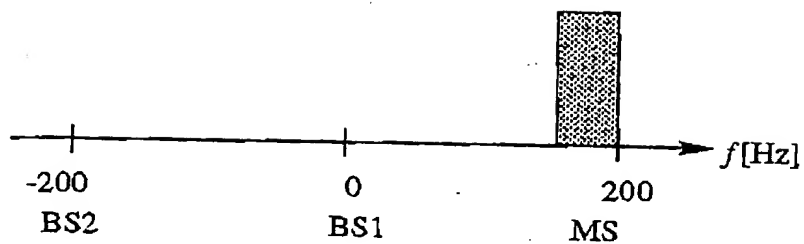


FIG. 14B

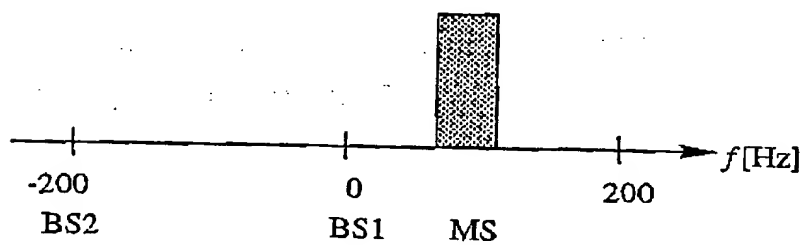


FIG. 14C

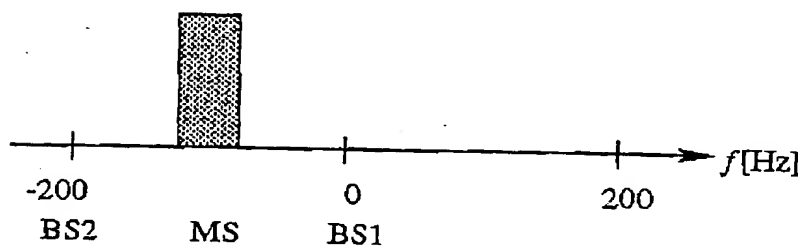
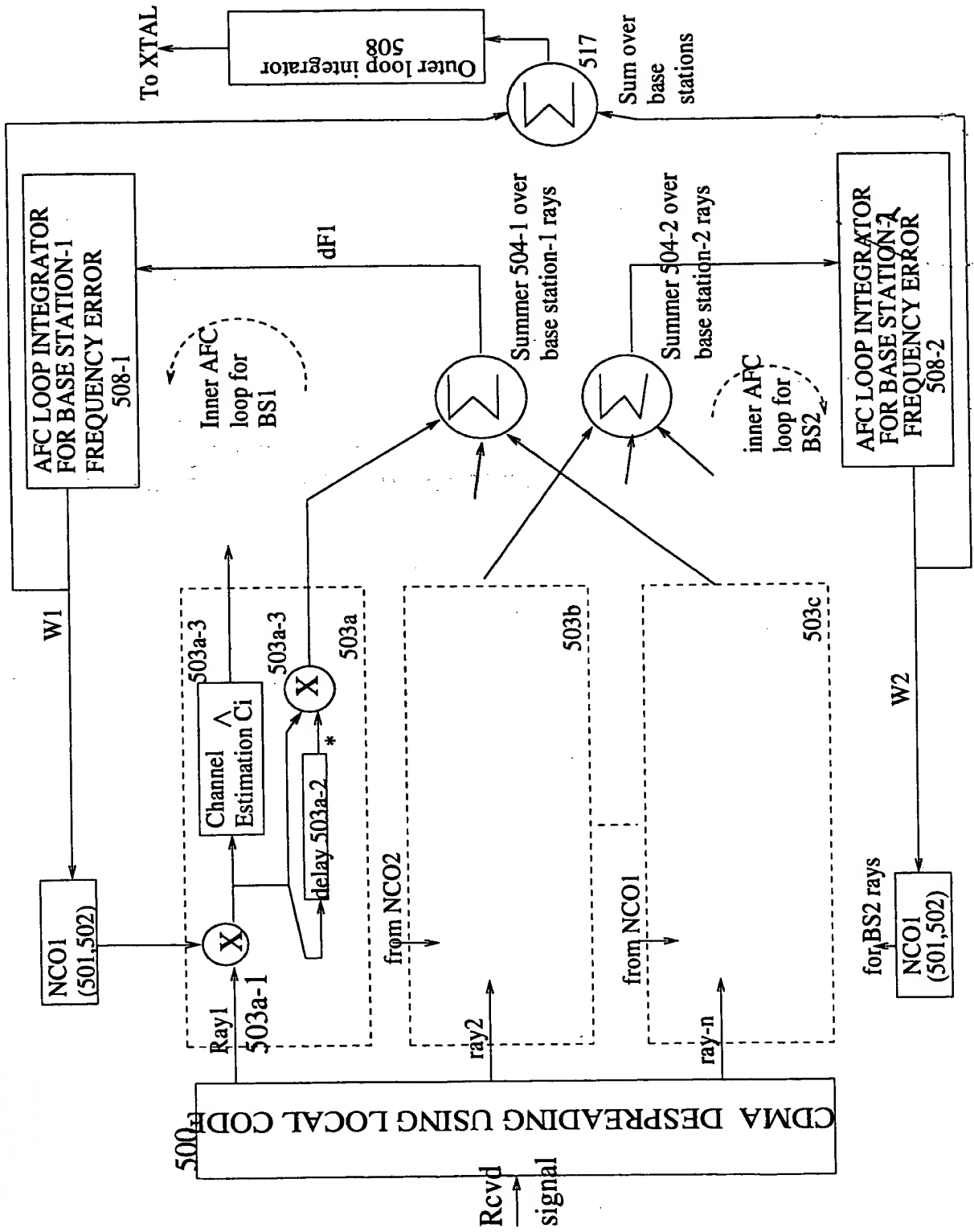


FIGURE 15A: FREQUENCY ERROR ESTIMATION FOR TWO BASE STATIONS



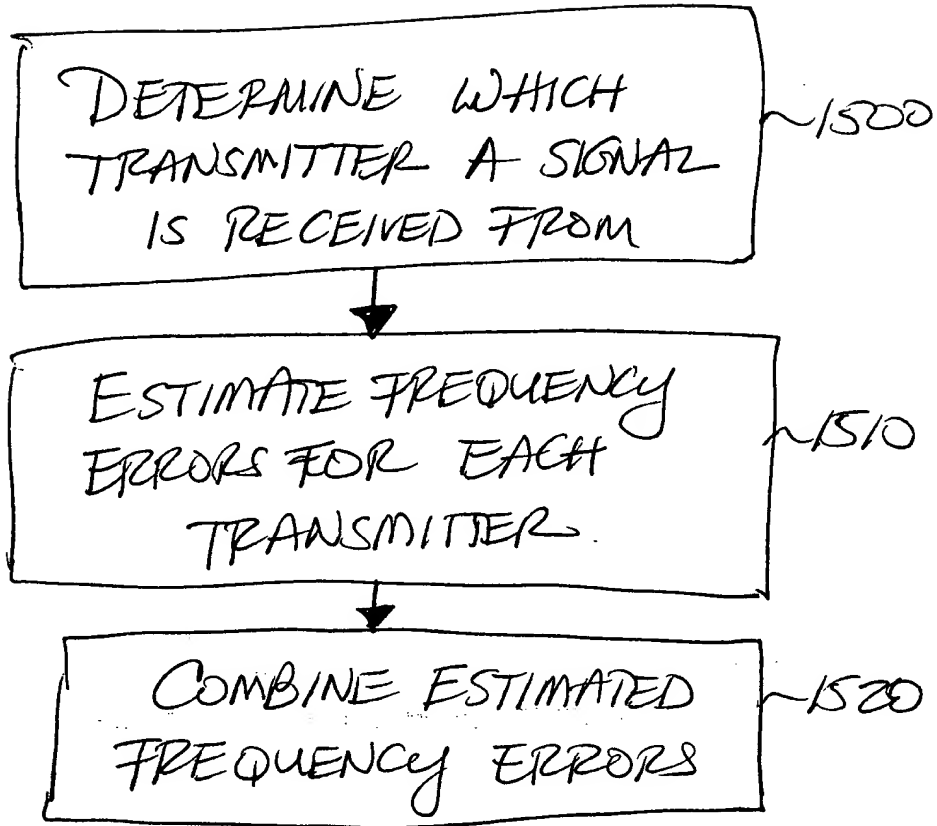


FIG. 15B